



HW

LEE0031-US

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: YING-HSIN LI, ET AL. Serial No.: 10/781,879 Filed: February 20, 2004 For: METHOD AND STRUCTURE OF DIODE	Art Unit: 2818 Examiner: Not Yet Assigned
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STATUS INQUIRY

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

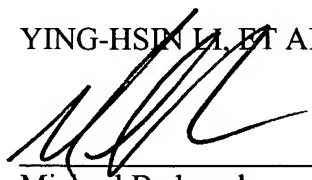
Applicant respectfully inquires as to the status of the above-identified patent application, and when the next communication from the United States Patent and Trademark Office regarding this application can be expected.

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Date: April 1, 2005

Respectfully submitted,

YING-HSIN LI, ET AL.

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